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Claims

[c1] What is claimed is:

1.A package structure for an image sensing integrated circuit component, comprising:

an image sensing integrated circuit chip having a light-receiving side and a backside, wherein said light-receiving side includes a plurality of bonding pads and a light-sensing area for converting light into electrical signals;

an asymmetrically coated glue frame formed on outskirts of said light-sensing area on said light-receiving side of said image sensing integrated circuit chip; and

a cover glass sealing said light-sensing area with said glue frame.

[c2] 2.The package structure for an image sensing integrated circuit component according to claim 1 wherein said light-sensing area includes a plurality of light sensing devices.

[c3] 3.The package structure for an image sensing integrated circuit component according to claim 2 wherein said light sensing devices are CMOS image sensors.

[c4] 4.The package structure for an image sensing integrated circuit component according to claim 2 wherein said light sensing devices are charge couple devices.

[c5] 5.The package structure for an image sensing integrated circuit component according to claim 1 wherein said image sensing integrated circuit chip is substantially rectangular shaped and therefore has four sides, and wherein said plural bonding pads are arranged along only one single side of said four sides of said image sensing integrated circuit chip.

[c6] 6.The package structure for an image sensing integrated circuit component according to claim 1 wherein said image sensing integrated circuit chip is substantially rectangular shaped and therefore has four sides, and wherein said plural bonding pads are arranged along only one two adjacent sides of said four sides of said image sensing integrated circuit chip.

[c7] 7.The package structure for an image sensing integrated circuit component according to claim 1 wherein said asymmetrically coated glue frame comprises at least a first side with a first width and a second side with a second width that is smaller than the first width.

[c8] 8.A package structure for an image sensing integrated

circuit component, comprising:

an image sensing integrated circuit chip having a light-receiving side and a backside, wherein said light-receiving side includes a plurality of bonding pads and a light-sensing area for converting light into electrical signals;

an asymmetrically coated black glue frame formed on outskirts of said light-sensing area on said light-receiving side of said image sensing integrated circuit chip; and

a cover glass sealing said light-sensing area with said glue frame.

[c9] 9.The package structure for an image sensing integrated circuit component according to claim 8 wherein said light-sensing area includes a plurality of light sensing devices.

[c10] 10.The package structure for an image sensing integrated circuit component according to claim 9 wherein said light sensing devices are CMOS image sensors.

[c11] 11.The package structure for an image sensing integrated circuit component according to claim 9 wherein said light sensing devices are charge couple devices.

[c12] 12.The package structure for an image sensing inte-

grated circuit component according to claim 1 wherein said image sensing integrated circuit chip is substantially rectangular shaped and therefore has four sides, and wherein said plural bonding pads are arranged along only one single side of said four sides of said image sensing integrated circuit chip.

[c13] 13.The package structure for an image sensing integrated circuit component according to claim 1 wherein said image sensing integrated circuit chip is substantially rectangular shaped and therefore has four sides, and wherein said plural bonding pads are arranged along only one two adjacent sides of said four sides of said image sensing integrated circuit chip.

[c14] 14.The package structure for an image sensing integrated circuit component according to claim 1 wherein said asymmetrically coated black glue frame comprises at least a first side with a first width and a second side with a second width that is smaller than the first width.